

Product Bulletin

October 2025

FPGA-PB-02023 Rev 1.0

IP End-of-Life (EOL) Notice 1H 2025

Lattice is announcing the End-of-Life (EOL) of some of the Ethernet IP Cores as part of our product portfolio consolidation. These IP have been integrated into the unified **Tri-Speed Ethernet IP**, **10G Ethernet IP** and **Double Data Rate (DDR) Memory IP Module** offerings to streamline support and development.

Retiring IP Cores (EOL)	EOL Announcement	End of Support (2 SW Releases after announcement)	Reason for EOL	Exit Plan	Migration Guidelines Summary
10G Ethernet PCS All versions: 1.0.0, 1.1.0, 1.3.0	October 2025	Radiant 2026.2	Merged into 10G	Transition to	- Corresponding IP web pages archived This Product
10G Ethernet MAC All versions: 1.0.0, 1.1.0	October 2025	Radiant 2026.2	Ethernet IP Core Ethernet IP		Bulletin also uploaded on the archived pages Existing OPNs
SGMII Gb Ethernet PCS All versions: 1.0.1, 1.1.1, 1.2.0, 1.3.0, 1.4.0, 1.5.2, 1.6.2, 1.7.1	October 2025	Radiant 2026.2	Merged into Tri-speed Ethernet IP Core	Transition to Tri-speed Ethernet IP	are still orderable for 2 SW releases. Stay tuned for the upcoming price book releases - IP will remain on the IP Server Catalog for up to two SW releases. Final removal will occur with Radiant version 2026.2.
DDR3 PHY All versions: 1.0.2, 1.1.1	October 2025	Radiant 2026.2	The same function is covered under Double Data Rate (DDR) Memory Module IP	Transition to Double Data Rate (DDR) Memory Module IP or DDR_MEM	

Migration Guidelines

End of Support

All IP cores marked as End-of-Life (EOL) will continue to appear in the IP Server Catalog for two software releases following their EOL announcement. After that period, they will be permanently removed and will no longer receive support from Technical Support or the Software Tool. Final removal is scheduled for **Radiant version 2026.2**, so we highly recommend starting to use the **superseding IP Cores** listed below.

How to Access the Superseding IP Cores?

To support migration from End-of-Life (EOL) IP cores, the following active IP cores serve as their recommended replacements. These superseding IP Cores are available in the Lattice Radiant IP Catalog. Please refer to the access paths below.

Retiring IP Cores (EOL)	Superseding IP Core	IP Catalog Name	Access Path in Lattice Radiant	Latest Version in Radiant 2025.1 SP1
10G Ethernet PCS 10G Ethernet MAC	10G Ethernet	10 Gb Ethernet	IP Catalog > IP on Server	3.3.1
SGMII Gb Ethernet PCS	Tri-speed Ethernet	Tri-speed Ethernet	IP Catalog > IP on Server	2.2.0
DDR3 PHY	DDR Memory Module	DDR_MEM	IP Catalog > IP on Local	2.1.0

Published Resources

The web pages for the superseded IP Cores have been consolidated into the following active IP Core pages, which also contain the most updated IP documentation (User Guide, Release Notes, and/or Technical Notes):

<u>10Gb Ethernet IP Core Page</u> – Now includes 10G Ethernet MAC and 10G Ethernet PHY IP cores as part of the merged functionality.

<u>Tri-Speed Ethernet IP Core Page</u> – Now includes SGMII Gb Ethernet PCS as part of the merged functionality.

Double Data Rate (DDR) Memory Module or DDR_MEM – No published IP page available, as IP modules do not have standalone IP pages.

Archived Resources

The following IP core pages have been archived in the <u>Lattice Solution Search</u>. This Product Bulletin can be downloaded from the Documentation section of the archived pages below, as well as from the <u>Lattice Radiant Page</u>.

10Gb Ethernet MAC IP Core page
10Gb Ethernet PCS IP Core page
SGMII and Gb Ethernet PCS IP Core page

Ordering Information

Since the IP Cores on the above table have only been merged into active IP Cores (10G Ethernet and Tri-Speed Ethernet), **below OPNs will remain available for purchase.** Please stay tuned for upcoming Price book updates.

10G Ethernet supersedes 10G Ethernet PCS and 10G Ethernet MAC	Part Number		
Device Family	Single Seat Annual	Single Seat Perpetual	
Avant-G	ETHER-10G-AVG-US	ETHER-10G-AVG-UT	
Avant-X	ETHER-10G-AVX-US	ETHER-10G-AVX-UT	
CertusPro-NX	ETHER-10G-CPNX-US	ETHER-10G-CPNX-UT	
MachXO5-NX	ETHER-10G-XO5-US	ETHER-10G-XO5-UT	

Tri-speed Ethernet supersedes SGMII Gb Ethernet PCS	Part Number		
Device Family	Single Seat Annual	Single Seat Perpetual	
Certus-N2	TS-MAC-CN2-US	TS-MAC-CN2-UT	
Avant-G	TS-MAC-AVG-US	TS-MAC-AVG-UT	
Avant-X	TS-MAC-AVX-US	TS-MAC-AVX-UT	
Avant-E	TS-MAC-AVE-US	TS-MAC-AVE-UT	
CrossLink-NX	TS-MAC-CNX-US	TS-MAC-CNX-UT	
CertusPro-NX	TS-MAC-CPNX-US	TS-MAC-CPNX-UT	
Certus-NX	TS-MAC-CTNX-US	TS-MAC-CTNX-UT	
MachXO5-NX	TS-MAC-XO5-US	TS-MAC-XO5-UT	
ECP5	TS-MAC-E5-US	TS-MAC-E5-UT	
LatticeECP3	TS-MAC-E3-US	TS-MAC-E3-UT4	
LatticeECP2	-	TS-MAC-P2-UT4	
LatticeECP2M	TS-MAC-PM-US	TS-MAC-PM-UT4	
LatticeEC/ECP	-	TS-MAC-E2-UT4	
LatticeSC/M	-	TS-MAC-SC-UT4	
LatticeXP2	-	TS-MAC-X2-UT4	
LatticeXP	-	TS-MAC-XM-UT4	

For the **DDR3 PHY IP Core**, the Ordering Part Numbers (OPNs) listed in the Ordering Information section of the <u>DDR3 PHY IP page</u> will remain available for purchase until further notice. Please note that its superseding IP, the DDR MEM Module, is available free of charge.

For any technical issues encountered with the recommended IP Cores, please raise a support ticket through https://www.latticesemi.com/en/Support/SubmitTechSupport

For questions related to this EOL announcement, please reach out to Marketing@latticesemi.com.

Regards,

Lattice IP Marketing Team