MPCS Module

IP Version: v1.9.0

Release Notes

FPGA-RN-02088-1.1

December 2025

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Contents

1. Introduction

This document contains the Release Notes for the MPCS Module. For specific details about the IP, refer to the following:

- MPCS Module User Guide (FPGA-IPUG-02118)

MPCS Module v1.9.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.2	• Removed LFMXO5-100T and LFMXO5-55T device support. • Added Attributes for PLLA, PLLB and PLLC.

MPCS Module v1.8.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	• Fixed RXEQ settings for 10GE protocol. • Added 81 MHz reference clock support for DP/eDP protocol.

MPCS Module Earlier Versions

IP Version	Summary of Changes
1.7.0	Fixed PCS-bypass mode testbench support.
1.6.0	Added JESD204B support.
1.5.0	• Removed PCIe support. • Updated Quad1 instantiation. • Added supported data rates for SLVS_EC.
1.4.0	• Added support for LFMXO5 devices. • Updated PCIe mode to fix dynamic rate change functionality.
1.3.0	• Added support for UT24CP devices. • Updated SERDES Tuning parameters based on characterization results.
1.2.0	• Added Secondary Skip Pattern setting. • Added Word Alignment Pattern Mask Code setting. • Added Adaptive Equalization and Algorithm for all three data rates. • Updated 6-Lane PCS for X2 and X6 integration. • Updated Modes and Number of Lanes for DP/eDP protocol.
1.1.0	• Added customer testbench. • Added support for G8B10B protocol. • Added support for dynamic selection of reference clock. • Added support to use External PLL in 10GE PCS mode. • Added SERDES tab. • Updated metadata to support a new IP Engine. • Updated 6-Lane PCS implementation. • Removed RXAUI protocol.
1.0.0	Initial release.

References

- MPCS Module User Guide (FPGA-IPUG-02118)
- CertusPro-NX web page
- MachXO5-NX web page
- Lattice Propel Design Environment web page
- Lattice Radiant Software web page
- Lattice Insights for Lattice Semiconductor training courses and learning plans

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