



# Timer/Counter IP

IP Version: v1.4.0

## Release Notes

FPGA-RN-02022-1.0

December 2024

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# 1. Introduction

This document contains the Release Notes for the Timer/Counter IP. For specific details about the IP, refer to the following:

- [Timer/Counter IP User Guide \(FPGA-IPUG-02139\)](#)

**Note:** These release notes document the changes from IP version 1.4.0 and Lattice Radiant™ software version 2024.2 and above.

## Timer/Counter IP v1.4.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	Fixed bug where the timeout is asserted when Software-Controlled Retrigger is disabled.

## Timer/Counter IP v1.3.1 and Below

Software	Software Version	Summary of Changes
Lattice Radiant	2024.1 and below	Refer to the Release Notes section in <i>introduction.html</i> in the Radiant software installation folder or the IP server for details on changes made in previous versions of this IP.

## References

- [Timer/Counter IP User Guide \(FPGA-IPUG-02139\)](#)
- [Avant-E](#) web page
- [Certus-N2](#) web page
- [Certus-NX](#) web page
- [CrossLink](#) web page
- [Timer/Counter IP Core](#) web page
- [Lattice Propel Design Environment](#) web page
- [Lattice Radiant Software](#) web page
- [Lattice Insights](#) web page for Lattice Semiconductor training courses and learning plans

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