

MDIO Leader IP

IP Version: v1.3.0

Release Notes

FPGA-RN-02029-1.2

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1. Introduction

This document contains the Release Notes for the MDIO Leader IP. For specific details about the IP, refer to the following:

• MDIO Leader IP User Guide (FPGA-IPUG-02223)

MDIO Leader IP v1.3.0

Software	Software Version	Summary of Changes
Lattice Radiant™	2025.2	Enabled SDC flow and updated timing constraints for easier integration by replacing set clock groups with set false paths.

MDIO Leader IP v1.2.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.1	 Added LFD2NX-35, LFD2NX-65, LFMXO5-35, LFMXO5-35T, LFMXO5-65, and LFMXO5-65T device support. Minor GUI enhancements.

MDIO Leader IP v1.1.1

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2	Added LN2-CT-20 device support.

MDIO Leader IP Earlier Versions

IP Version	Summary of Changes	
1.1.0	Lattice Nexus™ 2 device support.	
1.1.0	IP is provided at no additional cost with the Lattice Radiant software.	
1.0.0	Initial release.	

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References

- MDIO Leader IP User Guide (FPGA-IPUG-02223)
- Avant™-E web page
- Avant-G web page
- Avant-X web page
- CertusPro™-NX web page
- Certus™-N2 web page
- MachXO5™-NX web page
- MDIO Leader IP Core web page
- Lattice Solutions IP Cores web page
- Lattice Solutions Reference Designs web page
- Lattice Solutions Boards web page
- Lattice Solutions Demonstrations web page
- Lattice Propel™ Builder software web page
- Lattice Radiant FPGA design software web page
- Lattice Insights for Lattice Semiconductor training courses and learning plans



Technical Support Assistance

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