Deinterlacer IP

IP Version: v1.3.0

Release Notes

FPGA-RN-02056-1.0

December 2025

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Contents

1. Introduction

This document contains the Release Notes for the Deinterlacer IP. For specific details about the IP, refer to the following:

- Deinterlacer IP User Guide (FPGA-IPUG-02135)

Deinterlacer IP v1.3.0

Software	Software Version	Summary of Changes
Lattice Radiant	2025.2	• Added support for LFD2NX-9, LFD2NX-28, LFD2NX-15, LFD2NX-25, LFD2NX-35, and LFD2NX-65 devices. • Added support for LFMXO5-35, LFMXO5-35T, LFMXO5-65, and LFMXO5-65T devices. • Added support for Lattice Avant and Certus-N2 devices. • Updated testbench to fix simulation errors. • Migrated ldc constraints to pdc constraints with updated clock frequencies. • Removed IP license requirement.

Deinterlacer IP Earlier Versions

IP Version	Summary of Changes
1.2.1	Updated to fix compatibility issue with the Lattice Radiant software 2023.1.
1.2.0	Added support for UT24C, UT24CP, and LFMXO5 devices.
1.1.1	Added support for LIFCL-33 devices.
1.1.0	Added support for LFCPNX devices.
1.0.0	Initial release.

References

- Deinterlacer IP User Guide (FPGA-IPUG-02135)
- CrossLink-NX web page
- Certus-NX web page
- Certus-N2 web page
- CertusPro-NX web page
- MachXO5-NX web page
- Avant-E web page
- Avant-G web page
- Avant-X web page
- Deinterlacer IP Core web page
- Lattice Radiant Software web page
- Lattice Insights for Lattice Semiconductor training courses and learning plans

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For frequently asked questions, refer to the Lattice Answer Database at www.latticesemi.com/Support/AnswerDatabase.

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