



# AHB-Lite Interconnect Module

IP Version: v1.5.0

## Release Notes

FPGA-RN-02044-1.3

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## Inclusive Language

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# 1. Introduction

This document contains the Release Notes for the AHB-Lite Interconnect Module. For specific details about the IP, refer to the following:

- [AHB-Lite Interconnect Module User Guide \(FPGA-IPUG-02051\)](#)

## AHB-Lite Interconnect Module v1.5.0

Software	Software Version	Summary of Changes
Lattice Propel Builder Lattice Radiant	2025.2	Added support for LFMXO4 device.

## AHB-Lite Interconnect Module v1.4.0

Software	Software Version	Summary of Changes
Lattice Propel Builder Lattice Radiant	2025.1	<ul style="list-style-type: none"> <li>• Updated the default value of the <i>Full Address Decoding up to 1 kB</i> attribute.</li> <li>• Removed the Early Burst Termination Protection feature.</li> <li>• Updated the <b>Manager Priority Settings</b> tab.</li> <li>• Added support for LFD2NX-15, LFD2NX-25, LFD2NX-35, and LFD2NX-65 devices.</li> <li>• Added support for LFMXO5-35, LFMXO5-35T, LFMXO5-65, and LFMXO5-65T devices.</li> <li>• Documented the changes for earlier IP versions in the IP Release Notes.</li> </ul>

## AHB-Lite Interconnect Module v1.3.3

Software	Software Version	Summary of Changes
Lattice Propel Builder	2024.2	Added the <i>Early Burst Termination Protection</i> setting to prevent the module from terminating burst transactions early, which is useful for external subordinates that are unable to handle terminations initiated by the module.

## AHB-Lite Interconnect Module v1.3.2

Software	Software Version	Summary of Changes
Lattice Propel Builder	2024.2	<ul style="list-style-type: none"> <li>• Fixed the behavior for simultaneous access for multiple managers to the same subordinate.</li> <li>• Changed the default order values for the fixed priority arbiter scheme to start from 0.</li> <li>• Added support for MachXO5-NX, CertusPro-NX, Lattice Avant, and Certus-N2 devices.</li> </ul>

## AHB-Lite Interconnect Module Earlier Versions

IP Version	Summary of Changes
1.3.1	<ul style="list-style-type: none"> <li>• Adopted AMBA new inclusive terminology.</li> <li>• Removed the unsupported <i>Weighted Round Robin</i> option from the <i>Arbiter Scheme</i> property under the <b>Manager Priority Settings</b> tab.</li> </ul>
1.3.0	Fixed the behavior for cascaded interconnect instantiation.
1.2.0	Extended support for all FPGA devices.
1.1.4	Added support for MachXO3L and MachXO3LF devices.

IP Version	Summary of Changes
1.1.3	Added support for MachXO2 devices.
1.1.2	Added support for LFMNX devices.
1.1.1	Added an explicit definition of port type for the clock and reset ports.
1.1.0	Improved customizability by allowing you to enable or disable the connection between an external manager and an external subordinate.
1.0.2	Extended support for the Lattice Radiant software platform. You can retrieve this IP from the Lattice Radiant IP Catalog window.
1.0.1	Added support for LIFCL and LFD2NX devices.
1.0.0	Initial release.

## References

- [AHB-Lite Interconnect Module User Guide \(FPGA-IPUG-02051\)](#)
- [Mach-NX](#) web page
- [MachXO2](#) web page
- [MachXO3](#) web page
- [MachXO3D](#) web page
- [MachXO4](#) web page
- [MachXO5-NX](#) web page
- [CrossLink-NX](#) web page
- [CertusPro-NX](#) web page
- [Certus-NX](#) web page
- [Certus-N2](#) web page
- [iCE40 UltraPlus](#) web page
- [Avant-E](#) web page
- [Avant-G](#) web page
- [Avant-X](#) web page
- [AHB-Lite Interconnect Module](#) web page
- [Lattice Propel Builder](#) software
- [Lattice Radiant](#) FPGA design software
- [Lattice Diamond Software](#) web page
- [Lattice Insights](#) for Lattice Semiconductor training courses and learning plans

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