

JESD204B IP

IP Version: v1.2.0

Release Notes

FPGA-RN-02006-1.2

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1. Introduction

This document contains the Release Notes for the JESD204B IP and JESD204B Driver. For specific details about the IP and driver, refer to the following:

- JESD204B IP User Guide (FPGA-IPUG-02259)
- JESD204B Driver API Reference (FPGA-TN-02412)

JESD204B IP v1.2.0

Software	Software Version	Summary of Changes
Lattice Radiant™	2025.1	 Added constraint to IP configuration whereby the value of F × K ((Octets per Frame + 1) × (Frames per Multiframe + 1)) must be a multiple of 4. Added hardware example designs for Avant-X Versa Board. Initial driver release v25.01.00.

JESD204B IP v1.1.0

Software	Software Version	Summary of Changes
Lattice Radiant	2024.2.1	Enabled support on Avant™-G and Avant-X devices.

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Software	Software Version	Summary of Changes
Lattice Radiant	2024.1	Initial release.



References

- JESD204B IP User Guide (FPGA-IPUG-02259)
- JESD204B Driver API Reference (FPGA-TN-02412)
- CertusPro-NX web page
- Avant-G web page
- Avant-X web page
- JESD204B IP Core web page
- Lattice Radiant Software web page
- Lattice Propel Design Environment web page
- Lattice Solutions IP Cores web page
- Lattice Solutions Reference Designs web page
- Lattice Insights for Lattice Semiconductor training courses and learning plans



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