



March 28th - 31st

FREE of Charge Training Sessions

Target Audience:

LEC2 in close cooperation with **Lattice Semiconductor Inc.** is conducting a series of advanced technical trainings targeted to existing or future Lattice device users.

Multiple Webcast sessions are conducted to cover the following subjects:

- System Verilog for Synthesis
- Implementing PCI Express Designs with Lattice FPGAs
- System Verilog for Simulation
- Lattice Propel Builder
- TCL for NX-Devices
- The Importance of Clocking Architecture
- Timing Constraints
- Device Architecture to Enable Vision Implementations
- Plumerai's efficient Edge AI solution for CrossLink-NX
- FPGA Design Tech.
- CDC
- Generic Interfaces
- High-Speed Interfaces

Webinar Objective:

The objective of the TechWebs is to accelerate learning curve for users of Lattice FPGAs.

In this webinar series we will cover FPGA design and simulation techniques, device architecture and primitives, design timing constraint and closure. LEC2's training and education program is characterized by detailed technical content (not a marketing format) where we discuss design techniques and best practices based on expertise of professional trainers.

TIME (CEST)	Monday, March 28 th	Tuesday, March 29 th	Wednesday, March 30 th	Thursday, March 31 st
9:00-10:00	System Verilog For Synthesis (Theory) Dan Nicula (LEC2)	The Importance Of Clocking Architecture Mourad Zakhama (LEC2)		Generic Interfaces Eugen Krassin (LEC2)
10:30-11:30	System Verilog For Synthesis (Practice) Dan Nicula (LEC2)		Device Architecture To Enable Vision Implementations Mourad Zakhama (LEC2)	High-Speed Interfaces Eugen Krassin (LEC2)
12:00-13:00	Implementing PCI Express Designs With Lattice FPGAs Charles Gardiner	Lattice Propel Builder Pablo Leyva (Citrobits)	Plumerai's Efficient Edge AI Solution For CrossLink-NX Dmitriy Gusev (Plumerai)	
13:30-14:30	System Verilog For Synthesis (Theory) Dan Nicula (LEC2)	TCL For NX-Devices (Covering Radiant & Propel) Pablo Leyva (Citrobits)		
15:00-16:00	System Verilog For Synthesis (Practice) Dan Nicula (LEC2)			
15:30-16:30			Lattice Hardware Root of Trust Technologies, Cyber Resilience, and Supply Chain Protections Eric Sivertson (Lattice)	
16:30-17:30	Implementing PCI Express Designs With Lattice FPGAs Charles Gardiner	Timing Constraints (Theory) Eugen Krassin (LEC2)		
17:00-18:00			FPGA Design Technique Eugen Krassin (LEC2)	Generic Interfaces Eugen Krassin (LEC2)
18:00-19:00	Lattice Propel Builder Pablo Leyva (Citrobits)	Timing Constraints (Practice) Eugen Krassin (LEC2)		
18:30-19:30			CDC Eugen Krassin (LEC2)	High-Speed Interfaces Eugen Krassin (LEC2)
19:30-20:30	TCL For NX-Devices (Covering Radiant & Propel) Pablo Leyva (Citrobits)	The Importance Of Clocking Architecture Mourad Zakhama (LEC2)		
20:00-21:00			Device Architecture To Enable Vision Implementations Mourad Zakhama (LEC2)	Lattice IEC 62443 Industrial Security and Cyber Resiliency Overview Karl Wachswender (Lattice)

[Webinar Registration Link](#)



About Arrow:

Arrow Electronics guides innovation forward for over 180,000 of the world's leading manufacturers of technology used in homes, business and daily life.

A global provider of products, services, and solutions, Arrow aggregates electronic components and enterprise computing solutions for customers and suppliers in industrial and commercial markets. The company maintains a network of more than 302 locations worldwide.



About LEC2:

LEC2, the official authorized training partner of Lattice Semiconductor, offers a variety of free and paid comprehensive technical training courses for engineers and technicians.

Different training formats are available: on customer site, face-to-face, live and recorded webinars, shorties, custom training session based on customer needs. Check LEC2 training portfolio.

