LPTM21L
Evaluation Board

Thank you for selecting the LPTM21L Evaluation Board.

This board demonstrates the LPTM21L (Platform Manager 2), functioning either as a Central Controller or Hardware Expander, showing how multiple LPTM21L devices can be managed from a single design to monitor and control up to 40 voltage rails, as well as monitor temperatures and currents.

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1. Connect power from any USB source using the included USB mini cable to J2.
2. Seven of red LEDs are on (GPIO1 – GPIO6, and GPIO10) and three red LEDs are off (GPIO7 – GPIO9); indicating the ASC section is in the safe state
3. DIO1 thru DIO4 LEDs display four of the bits of the FPGA I²C address as set by the FPGA_I2C_ADX DIP switch (SW5).
4. The row of amber LEDs from DIO5 - DIO20 cycle back and forth.
5. Visit the web page for the LPTM21L Evaluation Board to download the User's Guide (including a full description of the default demo), demonstration source files and other related resources here: www.latticesemi.com/lptm21levn.
6. If you have further questions about this board, our software packages, or Lattice devices, you can contact Technical Support via the Lattice website at www.latticesemi.com/techsupport.

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