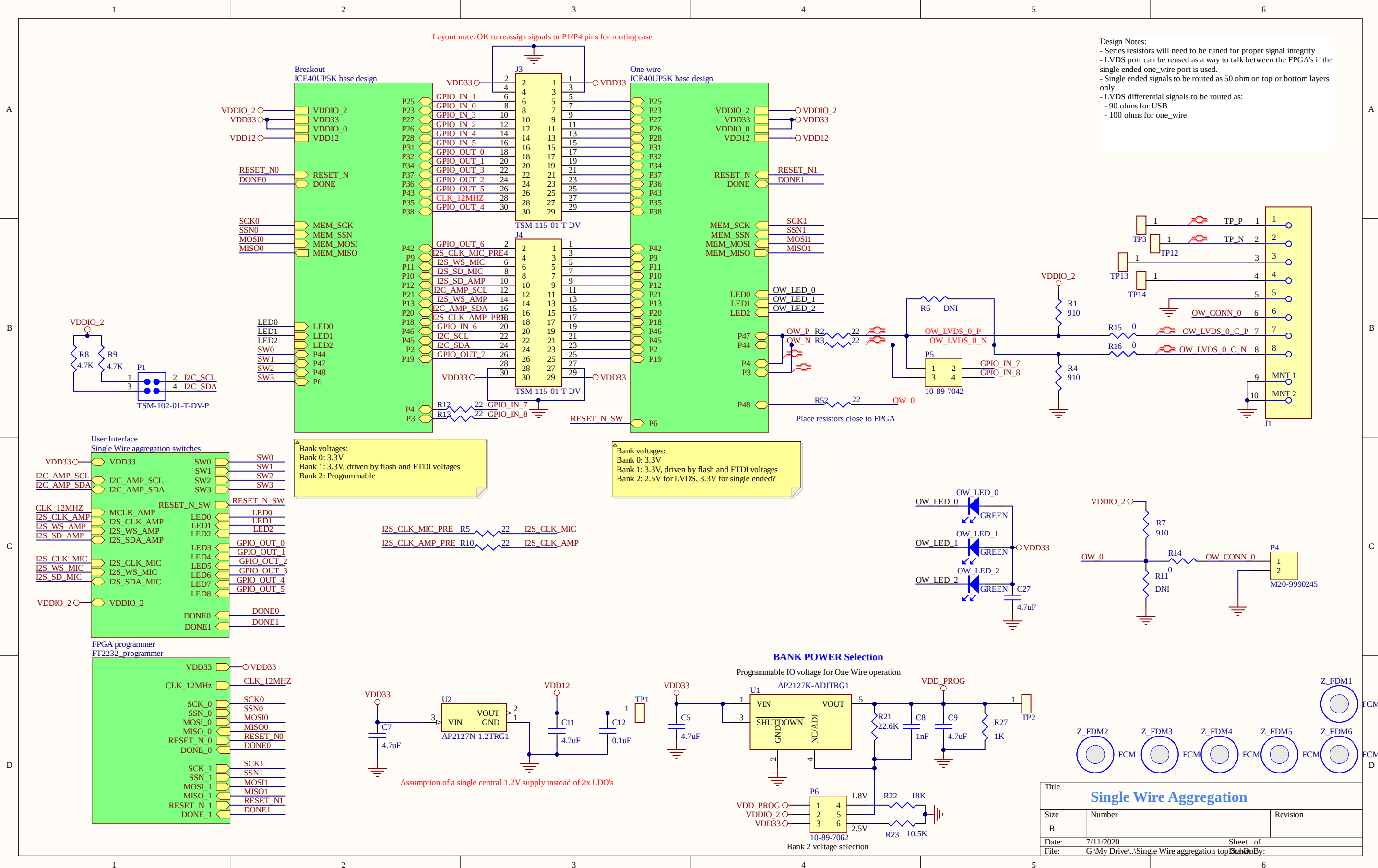
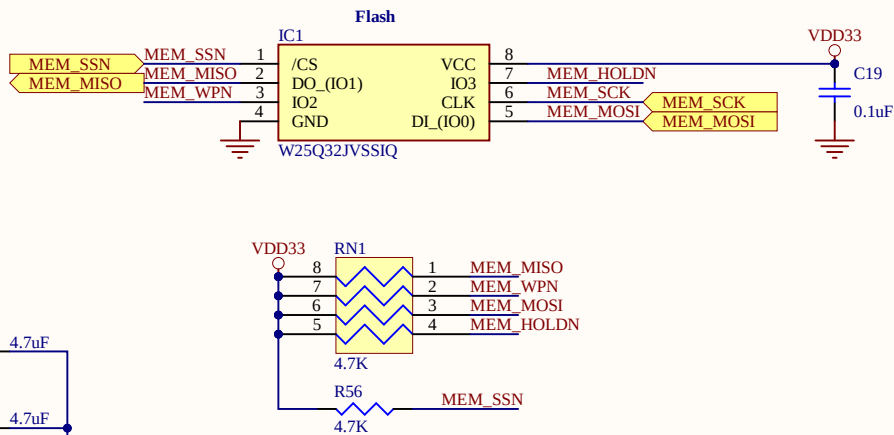
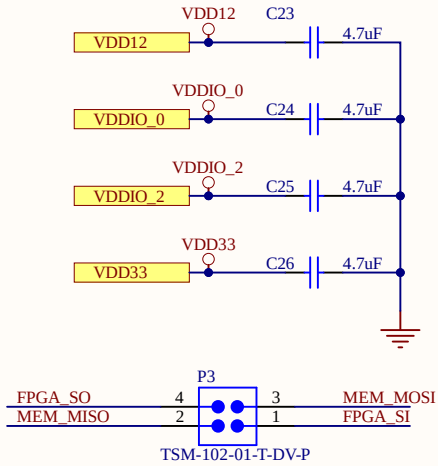
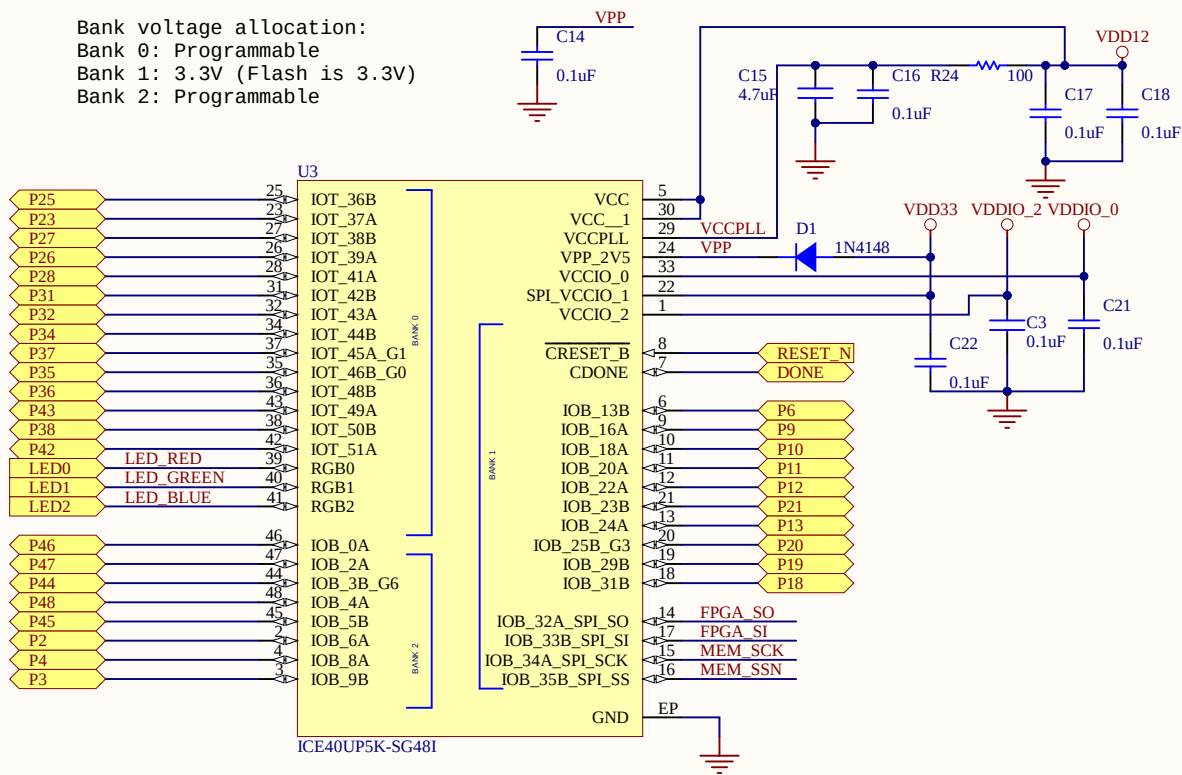


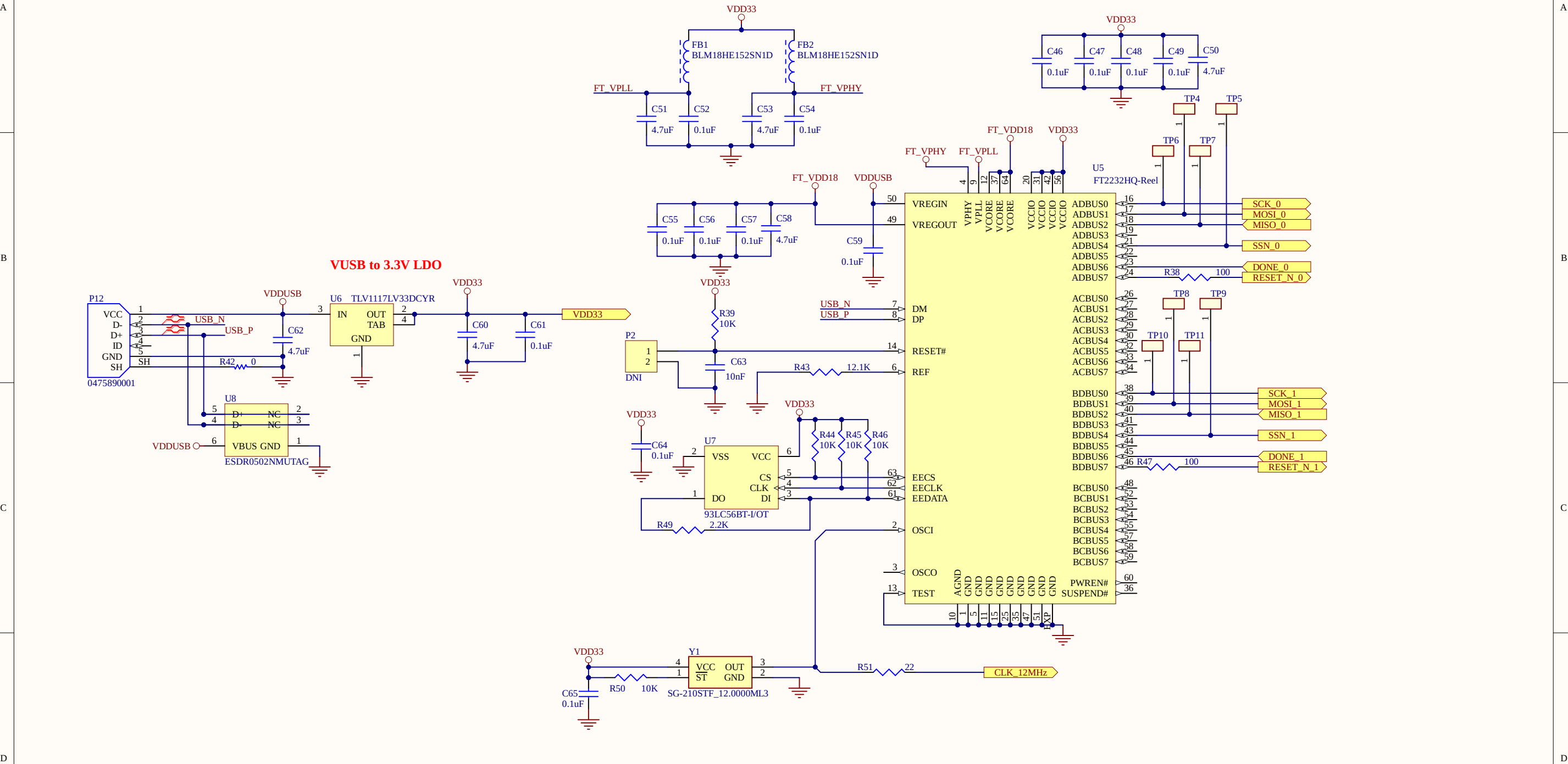


Bank voltage allocation:
Bank 0: Programmable
Bank 1: 3.3V (Flash is 3.3V)
Bank 2: Programmable



Note:
Short 1-2 and 3-4 to program Flash
Short 1-3 and 2-4 to program FPGA RAM

Title		
FPGA		
Size	Number	Revision
B		
Date:	7/11/2020	Sheet of
File:	G:\My Drive\ICE40UP5K base design\Sheet By:	



Title FT232 Programmer		
Size B	Number	Revision
Date: 7/11/2020	Sheet of	
File: G:\My Drive\...FT232_programmer.SchDoc	Drawn By:	