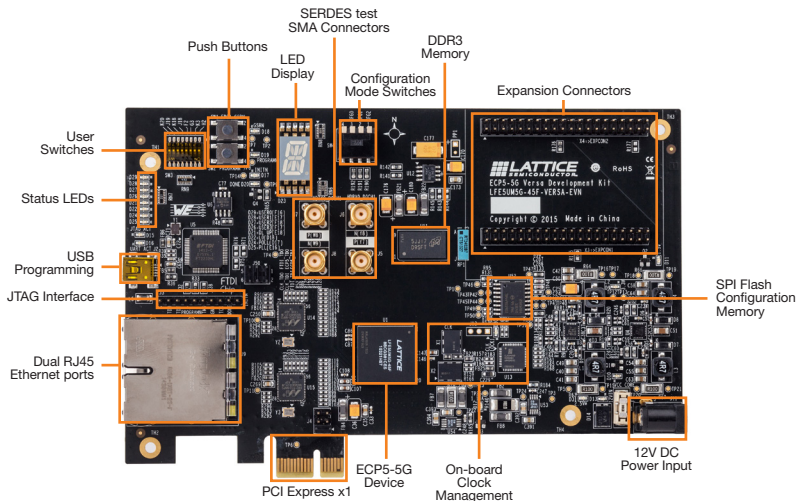This document provides a brief introduction on how to install and run the demos based on the ECP5-5G Versa Development Kit.

Check Kit Contents

The ECP5-5G Versa Development Kit contains the following items:

- ECP5-5G Versa Evaluation Board
- 12V AC/DC Adapter & International Plug Adapters
- USB Cable for Programming via PC (USB-A to Mini-B)
- Quick Start Guide

Using the ECP5-5G Versa Development Kit

The ECP5-5G Versa Board as shipped supports Master-SPI based configuration. The external SPI Flash Configuration Memory is pre-loaded with a demonstration program described below.

3 Installing the software

If you do not have Lattice Diamond design software (version 3.6 or later) installed on your PC, visit www.latticesemi.com/software for instructions on how to download the software. If you only need to program a bitstream file to the FPGA, you can install the Diamond Programmer Stand-alone, v3.6 or later.

4 Powering the Board & Observing the Demo Program

First, connect the supplied power adapter to the 12VDC power input (J11) on the board. A few LEDs should light, indicating the board is powered. The ECP5-5G will begin configuration from the external SPI memory (provided SW4 is in default state of 010). Configuration will complete almost immediately, at which time the Status LEDs and LED Display will be illuminated in a repeating pattern.

5 Programming the ECP5-5G Versa Board with Additional Demos

To update the ECP5-5G Versa Board with additional demos, connect the included USB-mini cable to your PC with the Diamond software installed. The ECP5-5G FPGA can be programmed to perform any number of functions.

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