

# **Smart Socket**

# **User Guide**

FPGA-UG-02046 Version 1.2



#### **Disclaimers**

Lattice makes no warranty, representation, or guarantee regarding the accuracy of information contained in this document or the suitability of its products for any particular purpose. All information herein is provided AS IS, with all faults and associated risk the responsibility entirely of the Buyer. Buyer shall not rely on any data and performance specifications or parameters provided herein. Products sold by Lattice have been subject to limited testing and it is the Buyer's responsibility to independently determine the suitability of any products and to test and verify the same. No Lattice products should be used in conjunction with mission- or safety-critical or any other application in which the failure of Lattice's product could create a situation where personal injury, death, severe property or environmental damage may occur. The information provided in this document is proprietary to Lattice Semiconductor, and Lattice reserves the right to make any changes to the information in this document or to any products at any time without notice.



### **Contents**

1.	Introdu	ction				
2.	Feature	25				
3.	Block D	Block Diagram				
4.	Board S	Specifications	<del>-</del>			
5.	Softwar	re Requirements	8			
6.	Progran	mming the Device	8			
(		eneric Programming				
(		oftware Requirements for Specific Device Families				
	6.2.1.	MachXO3 Device Family Programming				
	6.2.2.	ECP5 Device Family Programming				
	6.2.3.	L-ASC10 Device Programming				
	6.2.4.	iCE40 Device Family Programming	18			
	6.2.5.	CrossLink Device Family Programming				
7.	Orderin	ng Information				
Te	Technical Support					
Ар	pendix A.	Smart Socket Board Schematics	25			
		Debugging				
Re	vision Hist	tory	31			



# **Figures**

Figure 3.1. Smart Socket Programming Board Block Diagram	6
Figure 6.1. Getting Started	8
Figure 6.2. Scanning	
Figure 6.3. Unable to Identify Device	
Figure 6.4. Identifying Correct Device	10
Figure 6.5. Access Device Properties through Edit	10
Figure 6.6. Programming Options	
Figure 6.7. Bit File Selection	
Figure 6.8. Program Icon	
Figure 6.9. Design Menu	11
Figure 6.10. Programming in Progress	11
Figure 6.11. Successful Programming	12
Figure 6.12. ASC Socket – Scanned MachXO2 Device	13
Figure 6.13. ASC Socket – Operation	
Figure 6.14. ASC Socket – Device Properties	
Figure 6.15. ASC Socket – Add External ASC Device	
Figure 6.16. ASC Socket – ASC File Load Menu	
Figure 6.17. ASC Socket – Operation Menu	
Figure 6.18. Warning	
Figure 6.19. ASC Socket – Warning for MachXO2	17
Figure 6.20. ASC Socket – Ready to Program Step	17
Figure 6.21. ASC Socket – Programming	
Figure 6.22. ASC Socket – Programming Completed	
Figure 6.23. iCE40 Family – Scanning Failed	
Figure 6.24. iCE40 Family – Device Family List	
Figure 6.25. iCE40 Family – Select the Programming File	
Figure 6.26. iCE40 Family – Program Icon	
Figure 6.27. iCE40 Family – Programming Completed Successfully	
Figure 6.28. CrossLink Family – Scanning Failed	
Figure 6.29. CrossLink Family – Device Family List	
Figure 6.30. CrossLink Family – Select the Programming File	
Figure 6.31. CrossLink Family – Program Icon	
Figure 6.32. CrossLink Family – Programming Completed Successfully	
Figure A.1. USB Programming Interface	
Figure A.2. Voltage Regulator and LEDs	
Figure A.3. JTAG Bank and Connections	
Figure A.4. VCC Core, Bypass Caps, and Test Points	
Figure B.1. Failed to Scan Device	29
Figure B.2. Detect Cable (FTDI)	29
Figure B.3. Scanning Completed	30

5



### 1. Introduction

This document describes Smart Socket, the next generation programming solution from Lattice Semiconductor. Smart Socket replaces the legacy Lattice Model 300 and its associated Socket Adapters. Smart Socket uses the JTAG-based Lattice Diamond® Programmer or Lattice Radiant™ Programmer programming software that is also used with Lattice Semiconductor's popular evaluation and customer boards. The use of Diamond Programmer or Lattice Radiant Programmer depends on the target device. Standard ESD environment and procedures should be followed when working with loose devices and the Smart Socket.

### 2. Features

Each Smart Socket board is unique for a device family and package. Smart Socket boards have common features such as:

- Powered over simple USB cable
- Power switch to remove power from the socket
- Integrated FTDI USB interface to work directly with Lattice Programming tools
- Power indicator LEDs
- Convenient test points

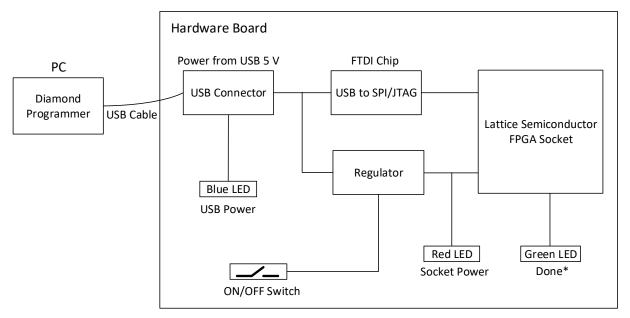


## 3. Block Diagram

Figure 3.1 shows the high level blocks of a Smart Socket board. The four major blocks on the board are:

- USB connector
- USB-to-SPI/JTAG communication bridge
- Board power regulator
  - Some product families, such as MachXO2™, have parts with different core supply voltage requirements. The Smart Socket board jumper (J2) allows you to select between 1.2 V and 3.3 V core supply voltage. The selected core supply voltage is indicated by LEDs located next to the jumper on the board.
- Lattice Semiconductor Socket (family specific)

A switch controls power to the socket. Three separate LEDs indicate USB power, Socket Power and Programming Done (Programming Done support varies by device family).



<sup>\*</sup>Note: Programming Done support depends on the device family.

Figure 3.1. Smart Socket Programming Board Block Diagram



# 4. Board Specifications

The outline dimension is the same for all Smart Socket boards. The dimensions of the socket vary based on target device family and package.

#### Board dimensions:

Width: 4 inchLength: 6 inchHeight: < 2 inch</li>

Electrical Specification:+5 V @ 500 mA or less (provided by USB cable)

The complete list of sockets is available at http://www.latticesemi.com/sockets.



## 5. Software Requirements

Smart Socket is supported by Lattice Diamond Programmer and Lattice Radiant Programmer. The latest version of the Lattice Diamond Programmer can be downloaded at

http://www.latticesemi.com/en/Products/DesignSoftwareAndIP/FPGAandLDS/LatticeDiamond.aspx.

The latest version of the Lattice Radiant Programmer can be downloaded at <a href="https://www.latticesemi.com/Products/DesignSoftwareAndIP/FPGAandLDS/Radiant">https://www.latticesemi.com/Products/DesignSoftwareAndIP/FPGAandLDS/Radiant</a>.

## 6. Programming the Device

Smart Socket works with the Lattice Diamond Programmer and Lattice Radiant Programmer using only a USB cable. Connect the cable from the Smart Socket to a PC and use the Programmer's Scan feature to establish the programming link.

The steps to program a device through Diamond Programmer are described below. The steps are similar when using Lattice Radiant Programmer.

### 6.1. Generic Programming

To program the device:

1. Launch the Lattice Diamond Programmer software. The **Diamond Programmer - Getting Started** dialog box appears as shown in Figure 6.1.

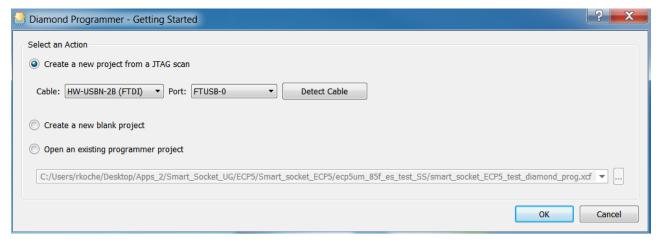


Figure 6.1. Getting Started

2. Click **OK.** The Diamond Programmer automatically starts scanning. The scanning page appears as shown in Figure 6.2.

© 2016-2022 Lattice Semiconductor Corp. All Lattice trademarks, registered trademarks, patents, and disclaimers are as listed at www.latticesemi.com/legal.

All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.



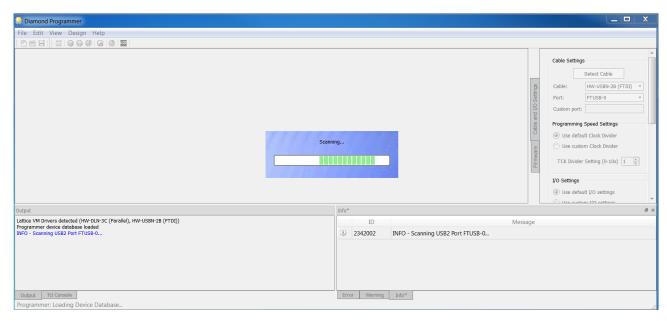


Figure 6.2. Scanning

When the device scanning is completed, the Diamond Programmer tool shows the device present in socket in the **Device** tab. If the Diamond Programmer is unable to identify the device in socket, the device family name is highlighted with a yellow background under **Device** as shown in Figure 6.3. Some device families may not support the scan operation. For details, see the Software Requirements for Specific Device Families section.

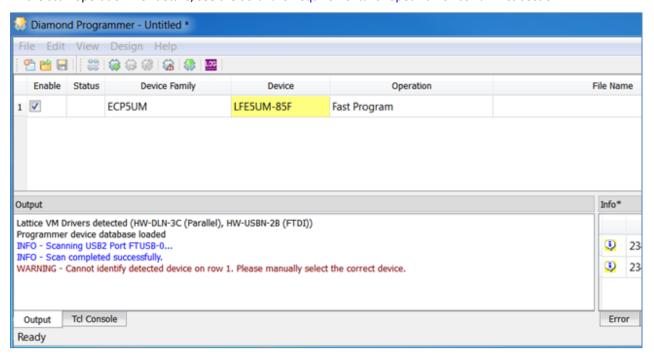


Figure 6.3. Unable to Identify Device

In such case, click in the yellow highlighted area and select the appropriate device by matching the device name on the package with the dropdown list in the **Device** tab. Select the correct device on the dropdown list, and the yellow background highlighting disappears as shown in Figure 6.4.



Figure 6.4. Identifying Correct Device

3. Verify that the desired **Operation** is specified, and update if necessary. To update the operation, select the device row so that it is highlighted in blue as shown in Figure 6.5. On the menu bar, click **Edit**, and on the dropdown menu click **Device Properties** as shown in Figure 6.5.

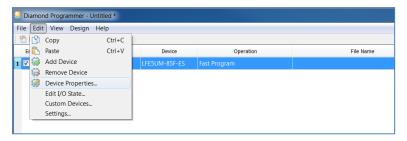


Figure 6.5. Access Device Properties through Edit

- 4. Select the appropriate programming file by clicking the **Browse** button in the **Programming Options** section as shown in Figure 6.6.
- 5. Click OK.

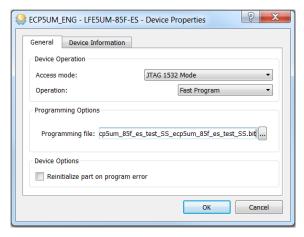


Figure 6.6. Programming Options

6. The selected file is shown under File Name in the Diamond Programmer main interface as shown in Figure 6.7.

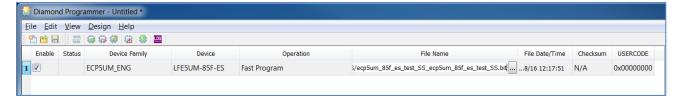


Figure 6.7. Bit File Selection

© 2016-2022 Lattice Semiconductor Corp. All Lattice trademarks, registered trademarks, patents, and disclaimers are as listed at www.latticesemi.com/legal.

All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.

10



7. To start programming the device, click the **Program** icon shown in Figure 6.8.



Figure 6.8. Program Icon

You can also click **Design** and select **Program** as shown in Figure 6.9.

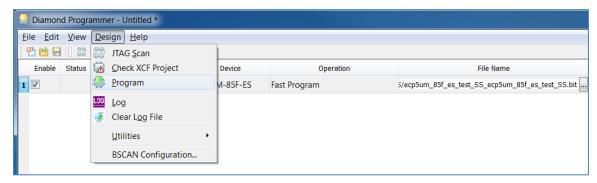


Figure 6.9. Design Menu

Figure 6.10 shows that the programming of device is in progress.

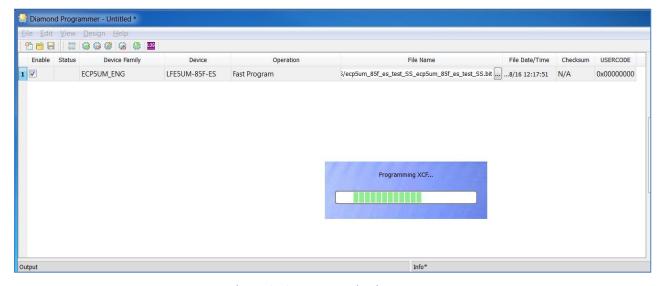


Figure 6.10. Programming in Progress

8. When the programming of the device is completed, the **Status** option changes to **PASS** and **Operation: successful** message appears in the **Output** console as shown in Figure 6.11.



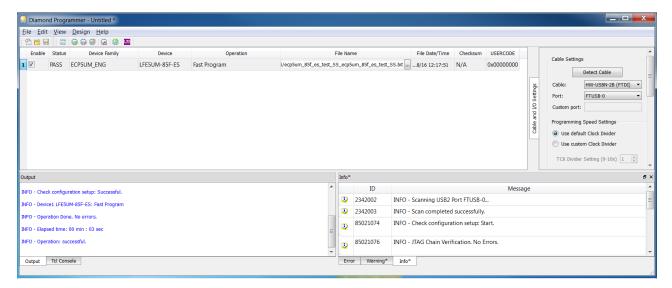


Figure 6.11. Successful Programming

On the board, the green *Done* LED is lit when the device is successfully programmed (*Done* LED behavior is device dependent, see the Software Requirements for Specific Device Families section for details).



### 6.2. Software Requirements for Specific Device Families

#### 6.2.1. MachXO3 Device Family Programming

The programming of MachXO3 device family follows steps similar to the process described in the Generic Programming section. *Done* is not a dedicated output in the MachXO3 device family. *Done* LED indication is not supported.

#### 6.2.2. ECP5 Device Family Programming

The programming of ECP5 device family follows steps similar to the process described in the Generic Programming section. When the programming of these devices is completed successfully, the *Done* LED is lit.

#### 6.2.3. L-ASC10 Device Programming

To program the ASC device:

Launch the Lattice Diamond Programmer software.
 The Diamond Programmer automatically starts scanning the device and detects the MachXO2 device as shown in Figure 6.12.

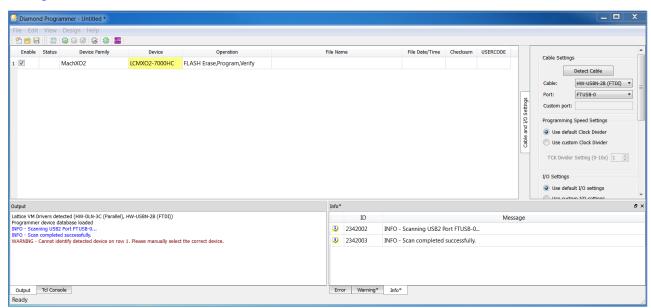


Figure 6.12. ASC Socket - Scanned MachXO2 Device

Double click in the box under Operation as shown in Figure 6.13.

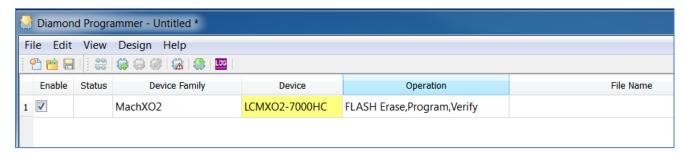


Figure 6.13. ASC Socket – Operation

3. The **Device Properties** dialog box appears as shown in Figure 6.14. In the **Access Mode** dropdown list, select **PTM Programming**. Note that in the main interface, **PTM Bypass** is indicated under **Operation**.

© 2016-2022 Lattice Semiconductor Corp. All Lattice trademarks, registered trademarks, patents, and disclaimers are as listed at www.latticesemi.com/legal.

All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.



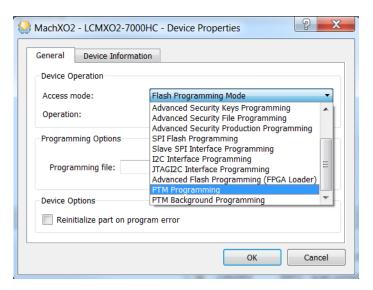


Figure 6.14. ASC Socket – Device Properties

4. Add an external ASC device, by clicking the button as shown in Figure 6.15. The **Device Properties** dialog box appears as shown in Figure 6.16.

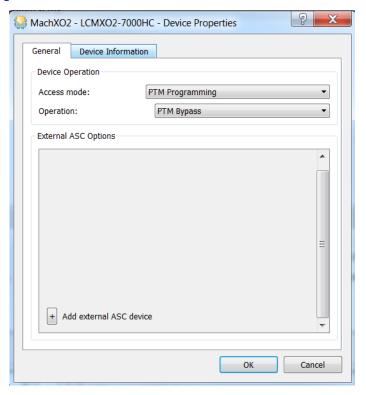


Figure 6.15. ASC Socket – Add External ASC Device



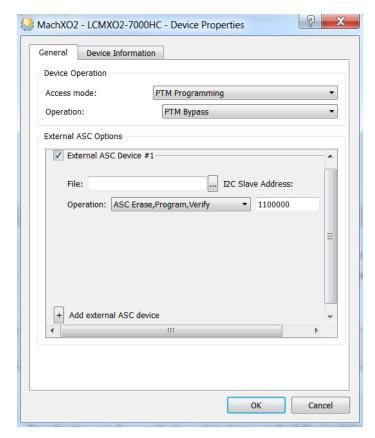


Figure 6.16. ASC Socket – ASC File Load Menu

- 5. Select the programming file by clicking the **Browse** button . In the **Device Properties** dialog box, select the \*ASCx.hex file.
- 6. From the Operation dropdown list, select ASC Erase, Program, Verify.



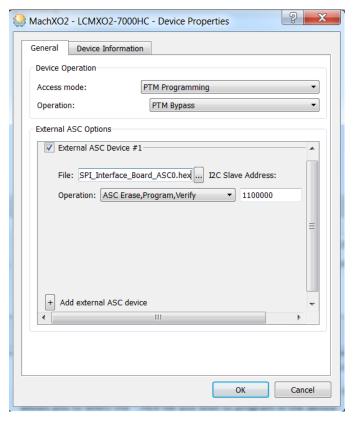


Figure 6.17. ASC Socket - Operation Menu

The warning shown in Figure 6.18 may appear if the selected external ASC device and the File targeting device do not match.

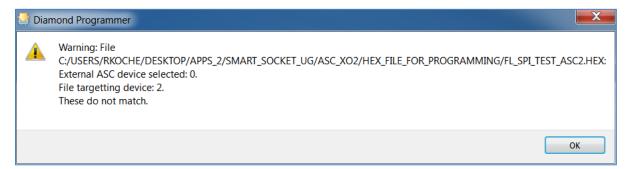


Figure 6.18. Warning

7. Click **OK**. The same warning appears in the output console window as shown Figure 6.19.

© 2016-2022 Lattice Semiconductor Corp. All Lattice trademarks, registered trademarks, patents, and disclaimers are as listed at www.latticesemi.com/legal.
All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.



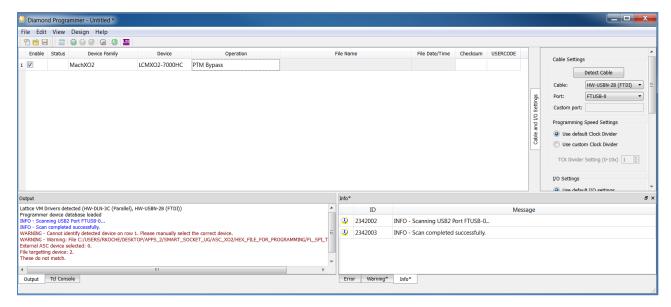


Figure 6.19. ASC Socket - Warning for MachXO2

Under **Operation**, **PTM Bypass** is indicated. The **File Name** field is greyed out (to prevent adding a new file) as shown in Figure 6.20.

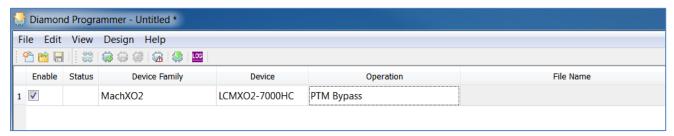


Figure 6.20. ASC Socket – Ready to Program Step

8. Program the ASC device through the MachXO2 device on the Smart Socket board by clicking the **Program** icon as shown in Figure 6.21.

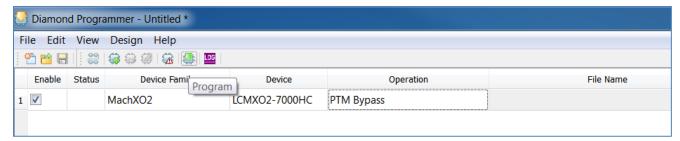


Figure 6.21. ASC Socket – Programming

9. When the programming of the device is completed, the **Status** option changes to **PASS** and **Operation: successful** message appears in the **Output** console as shown in Figure 6.22. During the programming activity the MXO2\_SDA and MXO2\_SCL LED light blink on the board showing communication between the MachXO2 and ASC devices. *Done* is not a dedicated output in the MachXO2 family. *Done* LED indication is not supported.

© 2016-2022 Lattice Semiconductor Corp. All Lattice trademarks, registered trademarks, patents, and disclaimers are as listed at www.latticesemi.com/legal.

All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.



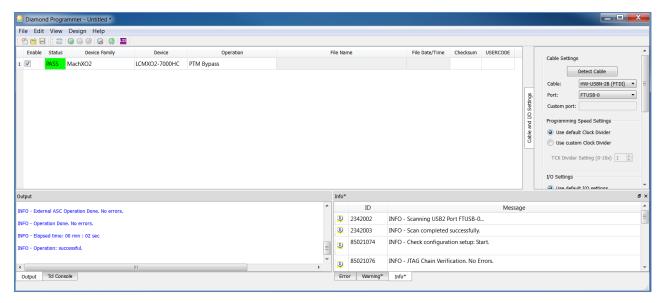


Figure 6.22. ASC Socket - Programming Completed

#### 6.2.4. iCE40 Device Family Programming

family of devices uses SPI interface for programming. See Figure 6.23.

To program the iCE40 device:

Launch the Lattice Diamond Programmer software.
 The scanning of the device fails because the Scan operation is supported over JTAG interface only, and the iCE40

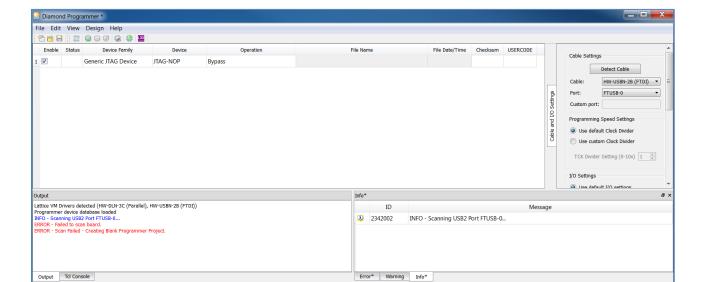


Figure 6.23. iCE40 Family - Scanning Failed

- 2. Manually select the device by choosing the following options as shown in Figure 6.24.
  - Device Family iCE5LP (select the appropriate device from the dropdown list)
  - Device iCE5LP1K (choose the size of the device based on the device present in the socket)

© 2016-2022 Lattice Semiconductor Corp. All Lattice trademarks, registered trademarks, patents, and disclaimers are as listed at www.latticesemi.com/legal.

All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.



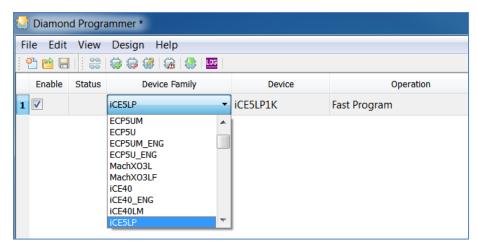


Figure 6.24. iCE40 Family - Device Family List

3. Select the programming file to program the iCE40 device by double clicking the **Browse** button under **File Name** as shown in Figure 6.25.



Figure 6.25. iCE40 Family - Select the Programming File

4. Click the **Program** icon to program the device as shown in Figure 6.26.

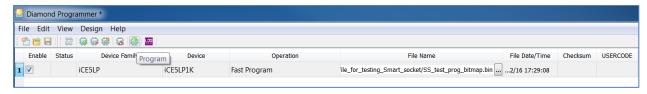


Figure 6.26. iCE40 Family - Program Icon

5. When the programming of the device is completed, the **Status** option changes to **PASS** and **Operation: successful** message appears in the **Output** console as shown in Figure 6.27.



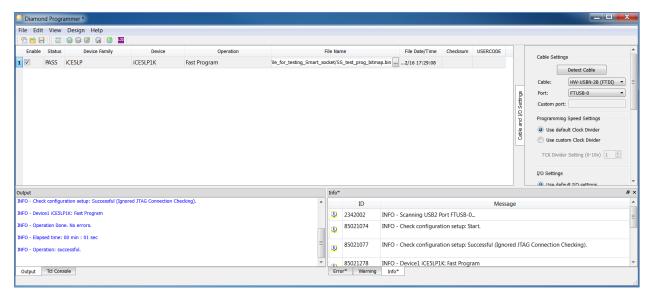


Figure 6.27. iCE40 Family – Programming Completed Successfully

20



#### 6.2.5. CrossLink Device Family Programming

To program the CrossLink Device:

1. Launch the Lattice Diamond Programmer software.

The scanning of the device fails as the Scan operation is supported over JTAG interface only, and the CrossLink family of devices uses SPI interface for programming. See Figure 6.28.

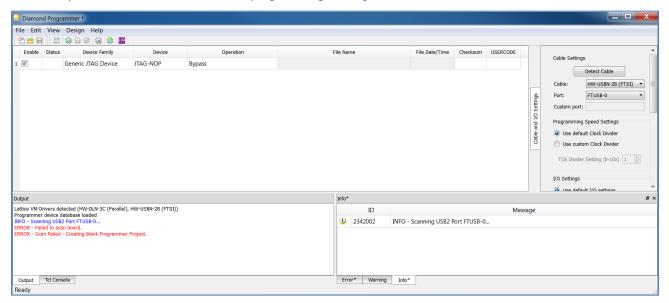


Figure 6.28. CrossLink Family - Scanning Failed

- 2. Manually select the device by choosing the following options as shown in Figure 6.29.
  - **Device Family** LIFMD (select the appropriate device from the dropdown list)
  - **Device** LIF-MD6000 or LIA-MD6000 (choose between industrial or automotive grade of CrossLink based on the device present in the socket)



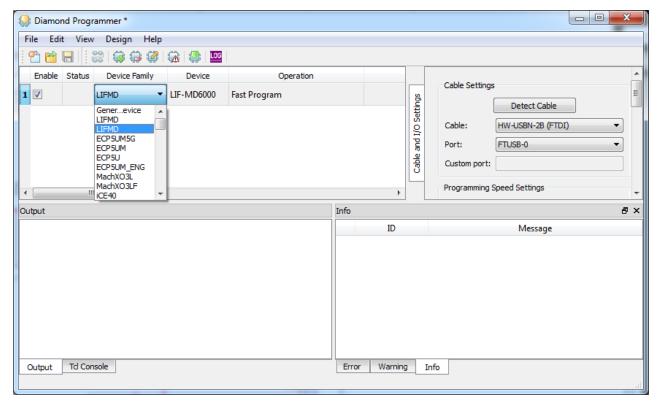


Figure 6.29. CrossLink Family – Device Family List

3. Select the programming file that you want to program in the CrossLink device by double clicking the **Browse** button under **File Name** as shown in **Figure 6.30**.

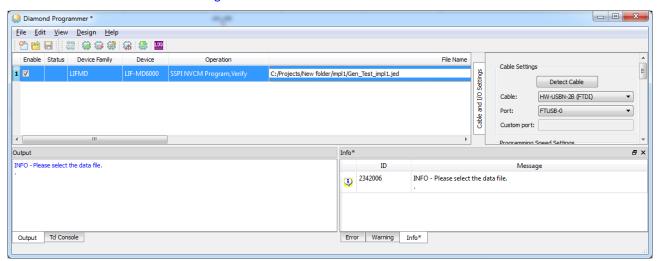


Figure 6.30. CrossLink Family - Select the Programming File

4. Click the **Program** icon to program the device as shown in Figure 6.31.

© 2016-2022 Lattice Semiconductor Corp. All Lattice trademarks, registered trademarks, patents, and disclaimers are as listed at www.latticesemi.com/legal.

All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.



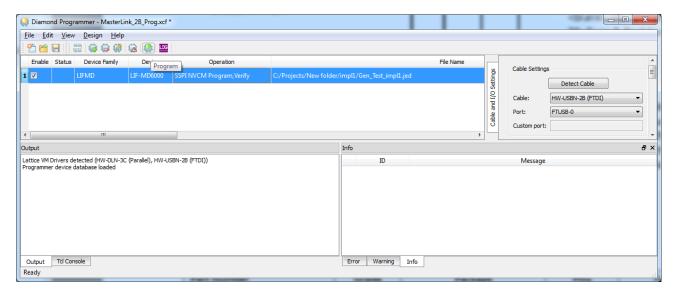


Figure 6.31. CrossLink Family - Program Icon

5. When the programming of the device is completed, the **Status** option changes to **PASS** and **Operation: successful** message appears in the **Output** console as shown in Figure 6.32.

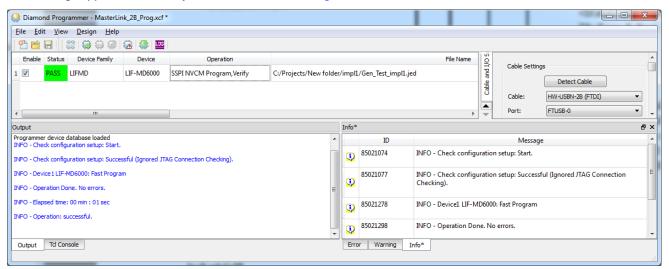


Figure 6.32. CrossLink Family - Programming Completed Successfully

## 7. Ordering Information

Ordering part numbers for a particular socket are available at http://www.latticesemi.com/sockets.



# **Technical Support**

For assistance, submit a technical support case at www.latticesemi.com/techsupport.



## **Appendix A. Smart Socket Board Schematics**

The following are representative schematics of a Smart Socket board. The FTDI and voltage regulator portions are the same across various Smart Socket boards.

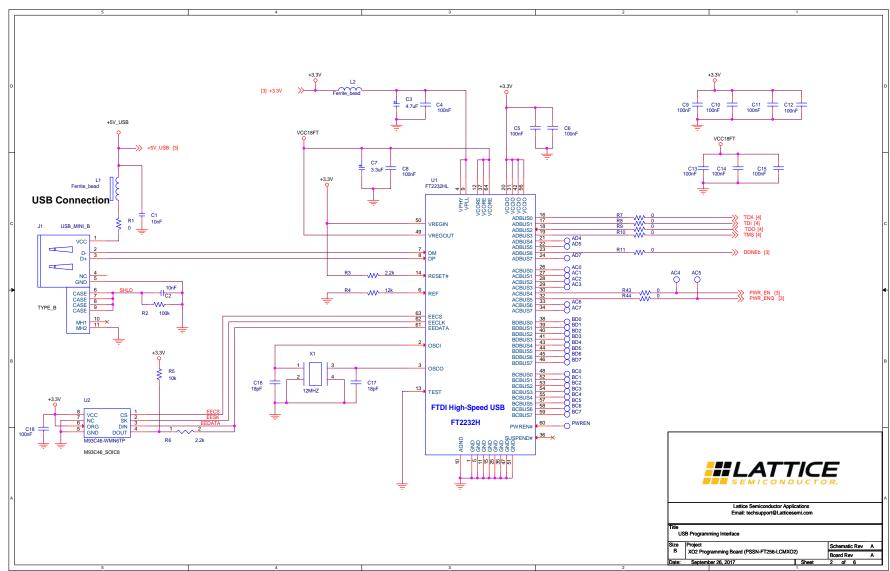


Figure A.1. USB Programming Interface

© 2016-2022 Lattice Semiconductor Corp. All Lattice trademarks, registered trademarks, patents, and disclaimers are as listed at www.latticesemi.com/legal.

All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.



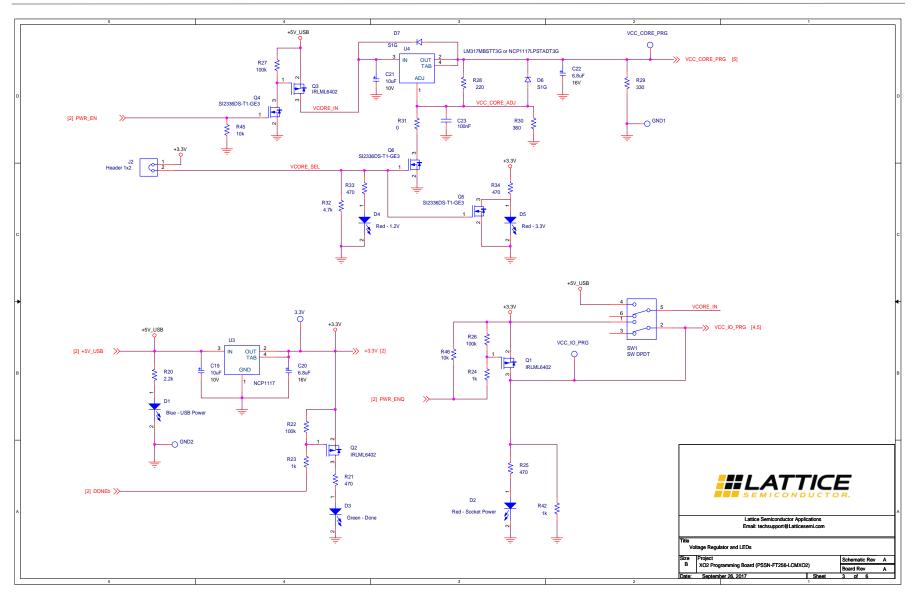


Figure A.2. Voltage Regulator and LEDs



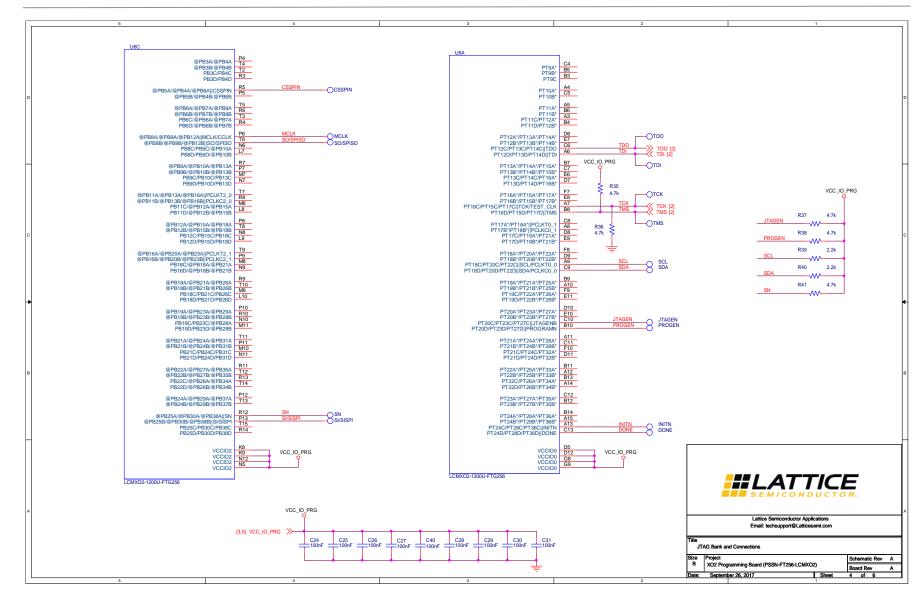


Figure A.3. JTAG Bank and Connections



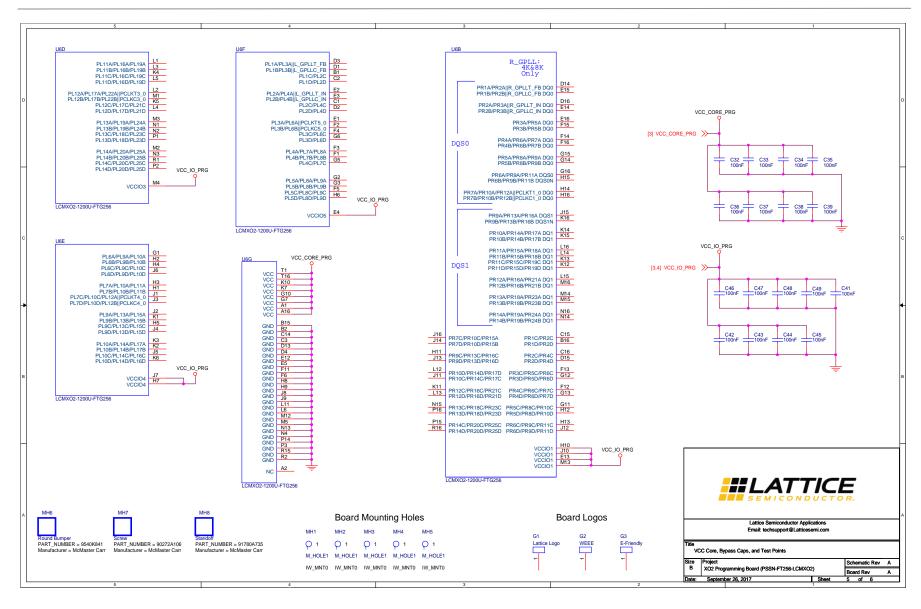


Figure A.4. VCC Core, Bypass Caps, and Test Points



## Appendix B. Debugging

Check the following if the programming fails:

- USB power supply
- ON/OFF switch is turned ON to provide power to the socket
- Device scanning failed

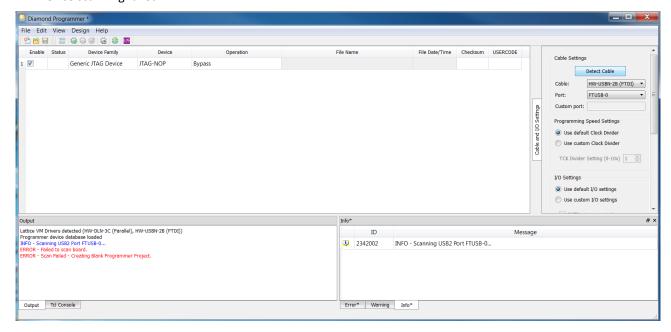


Figure B.1. Failed to Scan Device

Click the **Detect Cable** button and the Diamond Programmer starts detecting all the cables attached to USB ports. In the **Cable** dropdown menu, select the option which has the FTDI as shown in the figure below.

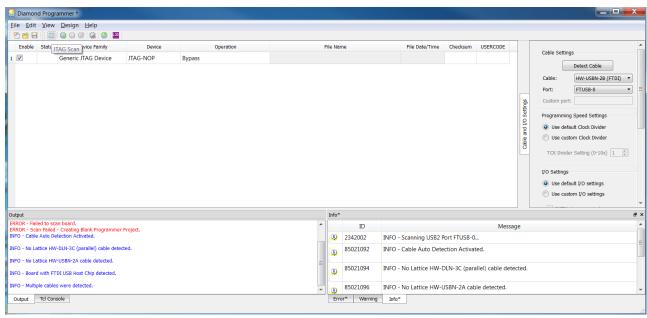


Figure B.2. Detect Cable (FTDI)

© 2016-2022 Lattice Semiconductor Corp. All Lattice trademarks, registered trademarks, patents, and disclaimers are as listed at www.latticesemi.com/legal.

All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notic



Ensure that the ON/OFF switch is turned to the ON position. The Diamond Programmer starts scanning the device in the socket. When the scan is completed, the exact device present in the socket appears in the **Device** tab as shown in Figure B.3.

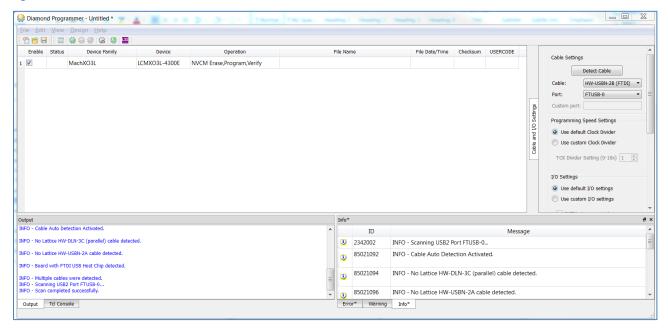


Figure B.3. Scanning Completed



# **Revision History**

### Revision 1.2, March 2022

Section	Change Summary
Disclaimers	Added this section.
Introduction	Added Lattice Radiant Programmer support.
Software Requirements	Added Lattice Radiant Programmer information. Moved procedures to succeeding section.
Programming the Device	Added this section.

#### Revision 1.1, June 2021

Section	Change Summary
All	Added CertusPro-NX device family.
Appendix A. Smart Socket Board Schematics	Added this section

#### Revision 1.0, February 2016

Section	Change Summary
All	Initial release.



www.latticesemi.com