



September 30, 2013

Subject: PCN#10A-13 Notification of Changes to the MachXO2 Family Data Sheet

Dear Lattice Customer,

Lattice is providing this notification of changes to the MachXO2™ Family Data Sheet.

Change Description

The updated MachXO2 Family Data Sheet ([DS1035](#) Version 02.2, released in September 2013) removes clock stretching feature from Hardened I²C IP Core section. Designs that do not use the hardened I²C core in User Slave mode are not affected by this PCN. Those customers using the hardened I²C core in User Slave mode on their MachXO2 devices should contact local field support immediately for instructions on next steps.

Affected Products

The Ordering Part Numbers (OPNs) affected by this PCN are available [here](#). This PCN also affects any custom devices (i.e. factory programmed, special test, etc.), which are derived from any of the devices listed in the table.

Datasheet Specifications

The revised MachXO2 Family Data Sheet ([DS1035](#) Version 02.2, released in September 2013) reflects the change described above and available on the Lattice website.

Software Support

Lattice Diamond™ Software Version 3.1 (planned to be released in December 2013) will disable clock stretching feature as a default.

PCN Timing

This datasheet change is effective immediately and retroactively.

Response

Those customers using the hardened I²C core in User Slave mode on their MachXO2 devices should contact local field support immediately for instructions on next steps.

Lattice PCNs are available on the [Lattice PCN web page](#). Please sign up to receive e-mail PCN alerts by registering [here](#). If you already have a Lattice web account and wish to receive PCN alerts, you can do so by logging into [your account](#) and making edits to your subscription options.

Contact

If you have any questions or require additional information, please contact pcn@latticesemi.com.

Sincerely,

Lattice Semiconductor PCN Administration